

Notice of References Cited	Application/Control No. 09/581,663	Applicant(s)/Patent Under Reexamination BECKER ET AL.	
	Examiner Shamim Ahmed	Art Unit 1746	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-6211092	04-2001	Tang et al	438/719
	C	US-5683908	11-1997	Miyashita et al	437/67
	D	US-4330384	05-1982	Okudaira et al	204/192
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	K	US-			
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	M	US-			

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf Et al, "Silicon Processing For The VLSI REA" 1986, Vol.1, pages 539-550.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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